CALL REPORT VENDOR SOFTWARE REQUIREMENTS
VALIDITY EDITS: June 30, 2002
(Federal Reserve Board and Federal Deposit Insurance Corporation)
(May 31, 2002, Revised June 11, 2002)

EDIT #	FORM	Target ITEM	EDIT TEST	Tolerance
0002	F & D	As-of-Date	As-of Date must be numeric and in YYMMDD format; month and day combination must = 0331, 0630, 0930, or 1231	- - -
0110	F	RI-1a3	Sum of (RI-1a1a through RI-1a2) must = RI-1a3	+- 4
0110	D	RI-1a6	Sum of (RI-1a1 through RI-1a5) must = RI-1a6	+- 3
0130	F	RI-1h	Sum of (RI-1a3 through RI-1g) must = RI-1h	+- 4
0130	D	RI-1h	Sum of (RI-1a6 through RI-1g) must = RI-1h	+- 4
0150	F	RI-2e	Sum of (RI-2a1a through RI-2d) must = RI-2e	+- 4
0150	D	RI-2e	Sum of (RI-2a1 through RI-2d) must = RI-2e	+- 3
0170	F & D	RI-3	(RI-1h minus RI-2e) must = RI-3	+- 1
0190	F & D	RI-5m	Sum of (RI-5a through RI-5l) must = RI-5m	+- 6
0210	F & D	RI-7e	Sum of (RI-7a through RI-7d) must = RI-7e	+- 2
0230	F & D	RI-8	(RI-3 + RI-5m + RI-6a + RI-6b) minus (RI-4 + RI-7e) must = RI-8	+- 3
0250	F & D	RI-10	(RI-8 minus RI-9) must = RI-10	+- 1
0270	F & D	RI-12	(RI-10 + RI-11) must = RI-12	+- 1
0330	F	RI-Mem3	RI-Mem3 must <= (RI-1a1f + RI-1b)	- - -

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0330	D	RI-Mem3	RI-Mem3 must <= (RI-1a5 + RI-1b)	- - -
0350	F & D	RI-Mem4	RI-Mem4 must <= RI-1d3	- - -
0390	D	RI-Mem6	RI-Mem6 must <= RI-1a5	- - -
0410	F & D	RI-Mem8d	If sum of (RI-Mem8a through RI-Mem8d) not= zero, then sum of (RI-Mem8a through RI-Mem8d) must = RI-5c	+- 2
0450	F & D	RI-Mem11 (Riad A530)	RI-Mem11 must be actual and must equal "1" (yes) or "0" (no)	- - -
			Riada530 ne null and (riada530 eq 1 or riada530 eq 0)	
0500	F & D	RI-A3	(RI-A1 + RI-A2) must = RI-A3	+- 1
0515[3]	F & D	RI-12	RI-A4 must = RI-12	- - -
0530	F & D	RI-A12	(RI-A3 + RI-A4 + RI-A5 + RI-A6 + RI-A7 + RI-A10 + RI-A11) minus (RI-A8 + RI-A9) must = RI-A12	+- 4
0600	F	RI-BI9A	Sum of (RI-BI1aA through RI-BI8bA) must = RI-BI9A	+- 9
0600	D	RI-BI9A	Sum of (RI-BI1aA through RI-BI8A) must = RI-BI9A	+- 6
0620	F	RI-BI9B	Sum of (RI-BI1aB through RI-BI8bB) must = RI-BI9B	+- 9
0660	F	RI-BIM1B	RI-BIM1B must < = (RI-BI4aB + RI-BI4bB + RI-BI7B)	- - -
0660	D	RI-BIM1B	RI-BIM1B must <= (RI-BI4B + RI-BI7B)	- - -
0680	F	RI-BIM2A	RI-BIM2A must <= sum of (RI-BI1aA through RI-BI1fA	- - -
0680	D	RI-BIM2aA	RI-BIM2aA must <= sum of (RI-BI1aA through RI-BI1eA	- - -

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0700	F	RI-BIM2B	RI-BIM2B must <= sum of (RI-BI1aB through RI-BI1fB	- - -
0700	D	RI-BIM2aB	RI-BIM2aB must <= sum of (RI-BI1aB through RI-BI1eB	- - -
0720	D	RI-BIM2bA	RI-BIM2bA must <= RI-BI2A	- - -
0740	D	RI-BIM2bB	RI-BIM2bB must <= RI-BI2B	- - -
0760	D	RI-BIM2cA	RI-BIM2cA must <= RI-BI4A	- - -
0780	D	RI-BIM2cB	RI-BIM2cB must <= RI-BI4B	- - -
0800	D	RI-BIM2dA	RI-BIM2dA must <= RI-BI8A	- - -
0820	D	RI-BIM2dB	RI-BIM2dB must <= RI-BI8B	- - -
0840	D	RI-BIM3A	RI-BIM3A must <= RI-BI7A	- - -
0860	D	RI-BIM3B	RI-BIM3B must <= RI-BI7B	- - -
1005[3]	F & D	RI-BI9B	RI-BII2 must = RI-BI9B	- - -
1015	F & D	RI-BII3	RI-BII3 must = (RI-BI9A minus RI-BII4)	+- 1
1025[3]	F & D	RI-4	RI-BII5 must = RI-4	- - -
1040	F & D	RI-BII7	(RI-BII1 + RI-BII2 + RI-BII5 + RI-BII6) minus (RI-BII3 + RI-BII4) must = RI-BII7	+- 3
1100	F	RI-D2	(RI-D1a minus RI-D1b) must = RI-D2	+- 1

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1120	F	RI-D3d	RI-D3a minus (RI-D3b + RI-D3c) must = RI-D3d	+- 1
1140	F	RI-D4	(RI-D2 + RI-D3d) must = RI-D4	+- 1
1160	F	RI-D6	(RI-D4 + RI-D5) must = RI-D6	+- 1
1180	F	RI-D8	(RI-D6 minus RI-D7) must = RI-D8	+- 1
2005[4]	F & D	RC-4c	RC-4c must = RI-BII7	+- 1
2020	F & D	RC-4d	(RC-4b minus RC-4c) must = RC-4d	+- 1
2040	F & D	RC-12	Sum of (RC-1a through RC-4a and RC-4d through RC-11) must = RC-12	+- 8
2060	F & D	RC-13a	(RC-13a1 + RC-13a2) must = RC-13a	+- 1
2080	F	RC-13b	(RC-13b1 + RC-13b2) must = RC-13b	+- 1
2100	F	RC-21	(RC-13a + RC-13b + RC-14a through RC-20) must = RC-21	+- 5
2100	D	RC-21	(RC-13a + RC-14a through RC-20) must = RC-21	+- 4
2120	F & D	RC-28	Sum of (RC-23 through RC-27) must = RC-28	+- 3
2125[4]	F & D	RC-28	RC-28 must = RI-A12	+- 1
2140	F & D	RC-29	(RC-21 + RC-22 + RC-28) must = RC-29	+- 1
2145	F & D	RC-29	RC-29 must equal RC-12	- - -
2200	F	RC-A1A	(RC-A1aB + RC-A1bB) must <= RC-A1A	- - -

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2220	F	RC-A2B	RC-A2B must <= (RC-A2aA + RC-A2bA)	- - -
2240	F	RC-A3B	RC-A3B must <= (RC-A3aA + RC-A3bA)	- - -
2260	F	RC-A4B	RC-A4B must <= RC-A4A	- - -
2280	D	RC-A5	If RC-12 (previous June) is >= $300 million (Wcode = 8 or 9), then sum of (RC-A1a through RC-A4) must = RC-A5	+- 3
2280	F	RC-A5A	Sum of (RC-A1A through RC-A4A) must = RC-A5A	+- 3
2300	D	RC-A5	If RC-12 (previous June) is >= $300 million (Wcode = 8 or 9), then RC-A5 must = (RC-1a + RC-1b)	+- 1
2300	F	RC-A5A	RC-A5A must = (RC-1a + RC-1b)	+- 1
2320	F	RC-A5B	Sum of (RC-A1aB through RC-A4B) must = RC-A5B	+- 2
2325	F	RC-A5B	RC-A5B must <= RC-A5A	- - -
2500	F & D	RC-2a	Sum of (RC-B1A through RC-B6bA) must = RC-B8A	+- 9
2505[3]	F & D	RC-2a	RC-B8A must = RC-2a	- - -
2530	F & D	RC-B8B	Sum of (RC-B1B through RC-B6bB) must = RC-B8B	+- 9
2550	F & D	RC-B8C	Sum of (RC-B1C through RC-B7C) must = RC-B8C	+- 9
2570	F & D	RC-2b	Sum of (RC-B1D through RC-B7D) must = RC-B8D	+- 9
2575[3]	F & D	RC-2b	RC-B8D must = RC-2b	- - -
2610	F & D	RC-BM1	RC-BM1 must <= (RC-2a + RC-2b)	- - -

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2620	F & D	RC-BM2b6 Rcfd/Rcon A560	If RC-N9C = 0, then the sum of (RC-BM2a1 through RC-BM2b6) must = sum of (RC-B1A through RC-B4a3A, RC-B5aA through RC-B6bA, RC-B1D through RC-B4a3D, and RC-B5aD through RC-B6bD)	+- 10
			If rcfd3507 eq 0 then (rcfda549 + rcfda550 + rcfda551 + rcfda552 + rcfda553 + rcfda554 + rcfda555 + rcfda556 + rcfda557 + rcfda558 + rcfda559 + rcfda560) eq (rcfd0211 + rcfd1289 + rcfd1294 + rcfd8496 + rcfd1698 + rcfd1703 + rcfd1709 + rcfdb838 + rcfdb842 + rcfdb846 + rcfdb850 + rcfdb854 + rcfdb858 + rcfd1737 + rcfd1742 + rcfd1287 + rcfd1293 + rcfd1298 + rcfd8499 + rcfd1702 + rcfd1707 + rcfd1713 + rcfdb841 + rcfdb845 + rcfdb849 + rcfdb853 + rcfdb857 + rcfdb861 + rcfd1741 + rcfd1746) (Note: same for 041 using "rcon")	
2621	F & D	RC-BM2b6 Rcfd/Rcon A560	Sum of (RC-BM2b1 through RC-BM2b6) must <= sum of (RC-B4a1A, RC-B4a2A, RC-B4a3A, RC-B4a1D, RC-B4a2D, and RC-B4a3D) plus $10 thousand)	
			(rcfda555 + rcfda556 + rcfda557 + rcfda558 + rcfda559 + rcfda560) le (rcfd1698 + rcfd1703 + rcfd1709 + rcfd1702 + rcfd1707 + rcfd1713 + 10) (Note: same for 041 using "rcon")	
2625	F & D	RC-BM2c2 Rcfd/Rcon A562	If RC-N9C = 0, then the sum of (RC-BM2c1 and RC-BM2c2 must = sum of (RC-B4b1A through RC-B4b3A and RC-B4b1D through RC-B4b3D)	+- 10
			If rcfd3507 eq 0 then (rcfda561 + rcfda562) eq (rcfd1714 + rcfd1718 + rcfd1733 + rcfd1717 + rcfd1732 + rcfd1736) (Note: same for 041 using "rcon")	
2630	F & D	RC-BM2d	RC-BM2d must <= sum of (RC-BM2a1 through RC-BM2c2)	- - -
2650	F & D	RC-BM4a	RC-BM4a must <= sum of (RC-B2aA through RC-B3A, RC-B5aA through RC-B6bA, RC-B2aC through RC-B3C, and RC-B5aC through RC-B6bC)	- - -
2670	F & D	RC-BM4b	RC-BM4b must <= sum of (RC-B2aB through RC-B3B, RC-B5aB through RC-B6bB, RC-B2aD through RC-B3D, and RC-B5aD through RC-B6bD)	- - -
3000	F	RC-CI1eB	Sum of (RC-CI1aB through RC-CI1eB) must <= RC-CI1A	- - -
3010	D	RC-CI2B	If RC-12 (previous June) >= $300 million (Wcode = 8 or 9), then sum of (RC-CI2a1A through RC-CI2c2A) must = RC-CI2B	+- 2
3020	F	RC-CI2aB	RC-CI2aB must <= (RC-CI2a1A + RC-CI2a2A)	- - -

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3040	F	RC-CI2bB	RC-CI2bB must <= RC-CI2Ba	- - -
3080	F	RC-CI3B	RC-CI3B must <= RC-CI3A	---
3090	D	RC-CI4B	If RC-12 (previous June) >= $300 million (Wcode = 8 or 9), then (RC-CI4aA + CI4bA) must = RC-CI4B	+- 1
3100	F	RC-CI4aB	RC-CI4aB must <= RC-CI4aA	- - -
3120	F	RC-CI4bB	RC-CI4bB must <= RC-CI4bA	- - -
3140	F	RC-CI6aB	RC-CI6aB must <= RC-CI6aA	- - -
3160	F	RC-CI6bB	RC-CI6bB must <= RC-CI6bA	- - -
3180	F	RC-CI6cB	RC-CI6cB must <= RC-CI6cA	- - -
3200	F	RC-CI7B	RC-CI7B must <= RC-CI7A	- - -
3220	F	RC-CI8B	RC-CI8B must <= RC-CI8A	- - -
3230	D	RC-CI9B	If RC-12 (previous June) >= $300 million (WCode = 8 or 9), then (RC-CI9aA + RC-CI9bA) must = RC-CI9B	+- 1
3240	F	RC-CI9bB	(RC-CI9aB + RC-CI9bB) must <= RC-CI9A	- - -
3250	D	RC-CI10B	If RC-12 (previous June) >= $300 million (Wcode = 8 or 9), then (RC-CI10aA + RC-CI10bA) must = RC-CI10B	+- 1
3260	F	RC-CI10B	RC-CI10B must <= (RC-CI10aA + RC-CI10bA)	- - -
3280	F	RC-CI11B	RC-CI11B must <= RC-CI11A	- - -
3300	F	RC-CI12A	RC-CI12A must = (RC-4a + RC-4b)	+- 1

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3300	D	RC-CI12B	RC-CI12B must = (RC-4a + RC-4b)	+- 1
3320	F	RC-CI12A	Sum of (RC-CI1A through RC-CI10bA) minus RC-CI11A must = RC-CI12A	+- 8
3340	F & D	RC-CI12B	Sum of (RC-CI1aB through RC-CI10B) minus RC-CI11B must = RC-CI12B	+- 9
3380	F	RC-CIM1	RC-CIM1 must <= (RC-CI12A + RC-CI11A) minus (RC-CI1c1B + RC-CI1c2aB + RC-CI1c2bB + RC-CI6aA + RC-CI6bA + RC-CI6cA)	- - -
3380	D	RC-CIM1	RC-CIM1 must <= (RC-CI12B + RC-CI11B) minus (RC-CI1c1B + RC-CI1c2aB + RC-CI1c2bB + RC-CI6aB + RC-CI6bB + RC-CI6cB)	- - -
3390	F & D	RC-CIM2a6 (Rcon A569)	Sum of (RC-CIM2a1 through RC-CIM2a6 and RC-N1c2aC) must = RC-CI1c2aB	+- 4
			(rcona564 + rcona565 + rcona566 + rcona567 + rcona568 + rcona569 + rconc229) eq rcon5367	
3395	F	RC-CIM2b6 (Rcfd A575)	Sum of (RC-CIM2b1 through RC-CIM2b6 and RC-N1aC through RC-N8bC minus RC-N1c2aC) must = sum of (RC-CI1A through RC-CI10bA minus RC-CI1c2aB)	+- 10
			(rcfda570 + rcfda571 + rcfda572 + rcfda573 + rcfda574 + rcfda575 + rcon3492 + rcon3495 + rcon5400 + rconc229 + rconc230 + rcon3501 + rcon3504 + rcfnb574 + rcfd5379 + rcfd5382 + rcfd1583 + rcfd1253 + rcfd1256 + rcfdb577 + rcfdb580 + rcfd5391 + rcfd5461 + rcfd1259 + rcfd1791 – rconc229) eq (rcfd1410 + rcfdb532 + rcfdb533 + rcfdb534 + rcfdb536 + rcfdb537 + rcfd1590 + rcfd1763 + rcfd1764 + rcfdb538 + rcfdb539 + rcfd2011 + rcfd2081 + rcfd2107 + rcfd1563 + rcfd2182 + rcfd2183 – rcon5367)	
3395	D	RC-CIM2b6 (Rcon A575)	Sum of (RC-CIM2b1 through RC-CIM2b6 and RC-N1aC through RC-N8C minus RC-N1c2aC) must = sum of (RC-CI1aB through RC-CI10B minus RC-CI1c2aB)	+- 10
			(rcona570 + rcona571 + rcona572 + rcona573 + rcona574 + rcona575 + rcon3492 + rcon3495 + rcon5400 + rconc229 + rconc230 + rcon3501 + rcon3504 + rconb836 + rcon1608 + rconb577 + rconb580 + rcon5391 + rcon5461 + rcon1228 – rconc229) eq (rcon1415 + rcon1420 + rcon1797 + rcon5367 + rcon5368 + rcon1460 + rcon1480 + rcon1288 + rcon1590 + rcon1766 + rconb538 + rconb539 + rcon2011 + rcon2081 + rcon2107 + rcon1563 + rcon2165 – rcon5367)	
3400	F & D	RC-CIM2c	RC-CIM2c must be less than or equal to the sum of RC-CIM2a1 through RC-CIM2b6	- - -
				- - -

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3420	F	RC-CIM3	RC-CIM3 must be less than or equal to the sum of RC-CI4aA, RC-CI4bA, and RC-CI9A	
3460	F & D	RC-CIM4	RC-CIM4 must be less than or equal to RC-CI1c2aB	- - -
3480	F	RC-CIM5	RC-CIM5 must <= RC-CI1A	- - -
3480	D	RC-CIM5	RC-CIM5 must <= Sum of (RC-CI1aB through RC-CI1eB)	- - -
3750	F & D	RC-CII1	(JUNE) RC-CII1 must be actual and must equal "1" (yes) or "0" (no) If mm-q1 = 06 then rcon6999 ne null and (rcon6999 eq 1 or rcon6999 eq 0)	- - -
3800	F & D	RC-CII3cB	(JUNE) Sum of (RC-CII3aB through RC-CII3cB) must <= RC-CI1eB	- - -
3820	F	RC-CII4cB	(JUNE) Sum of (RC-CII4aB through RC-CII4cB) must <= RC-CI4aB	- - -
3820	D	RC-CII4cB	(JUNE) If RC-CI4aA > 0, then Sum of (RC-CII4aB through RC-CII4cB) must <= RC-CI4aA	- - -
3830	D	RC-CII4cB	(JUNE) If RC-CI4aA = 0, then Sum of (RC-CII4aB through RC-CII4cB) must <= RC-CI4B	- - -
3840	F & D	RC-CII5	(JUNE) RC-CII5 must be actual and must equal "1" (yes) or "0" (no) If mm-q1 = 06 then rcon6860 ne null and (rcon6860 eq 1 or rcon6860 eq 0)	- - -
3850	F & D	RC-CII7cB	(JUNE) Sum of (RC-CII7aB through RC-CII7cB) must <= RC-CI1bB	- - -
3870	F & D	RC-CII8cB	(JUNE) Sum of (RC-CII8aB through RC-CII8cB) must <= RC-CI3B	- - -

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4000	F	RC-D12	If the sum of (RC-D1 through RC-D11b) > 0, then Sum of (RC-D1 through RC-D11b) must = RC-D12	+- 5
4000	D	RC-D12	If the sum of (RC-D1 through RC-D11) > 0, then Sum of (RC-D1 through RC-D11) must = RC-D12	+- 4
4015[3]	F & D	RC-5	RC-D12 must = RC-5	- - -
4030	F & D	RC-D15	If (RC-D13 + RC-D14) > 0, then (RC-D13 + RC-D14) must = RC-D15	+- 1
4045[3]	F & D	RC-15	RC-D15 must = RC-15	- - -
4200	F	RC-EI7A	Sum of (RC-EI1A through RC-EI6A) must = RC-EI7A	+- 3
4200	D	RC-E7A	Sum of (RC-E1A through RC-E6A) must = RC-E7A	+- 3
4220	F	RC-EI7B	RC-EI7B must <= RC-EI7A	- - -
4220	D	RC-E7B	RC-E7B must <= RC-E7A	- - -
4240	F	RC-EI7C	Sum of (RC-EI1C through RC-EI6C) must = RC-EI7C	+- 3
4240	D	RC-E7C	Sum of (RC-E1C through RC-E6C) must = RC-E7C	+- 3
4260	F	RC-EI7C	(RC-EI7A + RC-EI7C) must = RC-13a	+- 1
4260	D	RC-E7C	(RC-E7A + RC-E7C) must = RC-13a	+- 1
4280	F	RC-EIM1a	RC-EIM1a must <= (RC-EI1A + RC-EI1C)	- - -
4280	D	RC-EM1a	RC-EM1a must <= (RC-E1A + RC-E1C)	- - -
4300	F	RC-EIM1c2	(RC-EIM1c1 + RC-EIM1c2) must <= RC-EIM1b	- - -

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4300	D	RC-EM1c2	(RC-EM1c1 + RC-EM1c2) must <= RC-EM1b	- - -
4320	F	RC-EIM1d1	RC-EIM1d1 must <= RC-EIM1c1	- - -
4320	D	RC-EM1d1	RC-EM1d1 must <= RC-EM1c1	- - -
4340	F	RC-EIM1d2	RC-EIM1d2 must <= (RC-EIM1b minus RC-EIM1c1)	- - -
4340	D	RC-EM1d2	RC-EM1d2 must <= (RC-EM1b minus RC-EM1c1)	- - -
4360	F	RC-EIM2c	If RC-EIM2c > 0, then RC-EIM2c must be >= $100 thousand	- - -
4360	D	RC-EM2c	If RC-EM2c > 0, then RC-EM2c must be >= $100 thousand	- - -
4380	F	RC-EIM2c	Sum of (RC-EIM2a1 through RC-EIM2c) must = RC-EI7C	+- 2
4380	D	RC-EM2c	Sum of (RC-EM2a1 through RC-EM2c) must = RC-E7C	+- 2
4400	F	RC-EIM3a4	Sum of (RC-EIM3a1 through RC-EIM3a4) must = RC-EIM2b	+- 2
4400	D	RC-EM3a4	Sum of (RC-EM3a1 through RC-EM3a4) must = RC-EM2b	+- 2
4420	F	RC-EIM3b	RC-EIM3b must <= Sum of (RC-EIM3a1 through RC-EIM3a4)	- - -
4420	D	RC-EM3b	RC-EM3b must <= Sum of (RC-EM3a1 through RC-EM3a4)	- - -
4440	F	RC-EIM4a4	Sum of (RC-EIM4a1 through RC-EIM4a4) must = RC-EIM2c	+- 2
4440	D	RC-EM4a4	Sum of (RC-EM4a1 through RC-EM4a4) must = RC-EM2c	+- 2
4460	F	RC-EIM4b	RC-EIM4b must <= Sum of (RC-EIM4a1 through RC-EIM4a4	- - -
4460	D	RC-EM4b	RC-EM4b must <= Sum of (RC-EM4a1 through RC-EM4a4	- - -

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4480	F	RC-EIM4a1 through RC-EIM4b	If value reported for any item (RC-EIM4a1 through RC-EIM4b) > 0, then value must >= $100 thousand	- - -
4480	D	RC-EM4a1 through RC-EM4b	If value reported for any item (RC-EM4a1 through RC-EM4b) > 0, then value must >= $100 thousand	- - -
4520	F	RC-EII6	Sum of (RC-EII1 through RC-EII5) must = RC-EII6	+- 2
4535[3]	F	RC-13b	RC-EII6 must = RC-13b	- - -
4560	F	RC-EIIM1	RC-EIIM1 must <= RC-EII6	- - -
4620	F & D	RC-F5g	Sum of (RC-F5a through RC-F5g) must <= RC-F5	- - -
4630	F & D	RC-F5g	Sum of (RC-F1 through RC-F5) must = RC-F6	+- 3
4635[3]	F & D	RC-11	RC-F6 must = RC-11	- - -
4720	F & D	RC-G4g	Sum of (RC-G4a through RC-G4g) must <= RC-G4	- - -
4730	F & D	RC-G5	Sum of (RC-G1a through RC-G4) must = RC-G5	+- 2
4735[3]	F & D	RC-20	RC-G5 must = RC-20	- - -
4800	F	RC-H1	RC-H1 must <= RC-9	- - -
4810	F	RC-H2	RC-H2 must <= RC-18	- - -
4820	F	RC-H3	RC-H3 must <= RC-3b	- - -

EDIT #	FORM	Target ITEM	EDIT TEST	Tolerance
4830	F	RC-H4	RC-H4 must be <= RC-14b	- - -
4840	F	RC-H5	RC-H5 must <= RC-16	- - -
4850	F	RC-H7	If RC-H7 > 0, then RC-H6 must = 0	- - -
4860	F	RC-H7	If RC-H6 > 0, then RC-H7 must = 0	- - -
4870	F	RC-H8	(RC-H6 + RC-H8) must <= RC-12	- - -
4880	F	RC-H9	(RC-H7 + RC-H9) must <= RC-21	- - -
4885	F	RC-H9	(RC-H7 + RC-H9) must <= (RC-H6 + RC-H8)	- - -
4890	F	RC-H10	RC-H10 must <= (RC-B1A + RC-B1C)	- - -
4900	F	RC-H11	RC-H11 must <= (RC-B2aA + RC-B2bA + RC-B2aC + RC-B2bC)	- - -
4910	F	RC-H12	RC-H12 must <= (RC-B3A + RC-B3C)	- - -
4920	F	RC-H13a1	RC-H13a1 must <= (RC-B4a1A + RC-B4a2A + RC-B4a1C + RC-B4a2C)	- - -
4930	F	RC-H13a2	RC-H13a2 must <= (RC-B4a3A + RC-B4a3C)	- - -
4940	F	RC-H13b1	RC-H13b1 must <= (RC-B4b1A + RC-B4b1C)	- - -
4950	F	RC-H13b2	RC-H13b2 must <= (RC-B4b2A + RC-B4b3A + RC-B4b2C + RC-B4b3C)	- - -
4960	F	RC-H15	(RC-H14 + RC-H15) must <= Sum of (RC-B5aA through RC-B6bA + RC-B5aC through RC-B6bC)	- - -
4970	F	RC-H16	RC-H16 must <= RC-B7C	- - -

EDIT #	FORM	Target ITEM	EDIT TEST	Tolerance
4980	F	RC-H17	Sum of (RC-H10 through RC-H16) must = RC-H17	+- 5
4982	F	RC-H17	RC-H17 must <= (RC-2a + RC-B8C)	- - -
5020[1]	F	RC-I1	For Respondents with IBFs (WCode = 1 or 3), (RC-H8 + RC-I1) must <= RC-12	- - -
5030[1]	F	RC-I2	For Respondents with IBFs (WCode = 1 or 3), (RC-H9 + RC-I2) must <= RC-21	- - -
5100	F & D	RC-L2a	RC-L2a must <= RC-L2	- - -
5120	F & D	RC-L3a	RC-L3a must <= RC-L3	- - -
5140	F & D	RC-L9e	Sum of (RC-L9a through RC-L9e) must <= RC-L9	- - -
5160	F & D	RC-L10e	Sum of (RC-L10a through RC-L10e) must <= RC-L10	- - -
5180	F & D	RC-L14A	Sum of (RC-L12aA through RC-L12eA) must = (RC-L13A + RC-L14A)	+- 4
5200	F & D	RC-L14B	Sum of (RC-L12aB through RC-L12eB) must = (RC-L13B + RC-L14B)	+- 4
5220	F & D	RC-L14C	Sum of (RC-L12aC through RC-L12eC) must = (RC-L13C + RC-L14C)	+- 4
5240	F & D	RC-L14D	Sum of (RC-L12aD through RC-L12eD) must = (RC-L13D + RC-L14D)	+- 4
5250	F & D	RC-L14aA	RC-L14aA must <= RC-L12eA	- - -
5260	F & D	RC-L14aA	RC-L14aA must <= RC-L14A	- - -

EDIT #	FORM	Target ITEM	EDIT TEST	Tolerance
5400	F & D	RC-10b	(RC-M2a + RC-M2b + RC-M2c) must = RC-M2d	+- 1
5415[3]	F & D	RC-10b	RC-M2d must = RC-10b	- - -
5430	F	RC-M3c	Sum of (RC-M3a through RC-M3b6) must = RC-M3c	+- 3
5430	D	RC-M3c	Sum of (RC-M3a through RC-M3b5) must = RC-M3c	+- 3
5445[3]	F & D	RC-7	RC-M3c must = RC-7	- - -
5460	F & D	RC-M4c	(RC-M4a + RC-M4b) must = RC-M4c	+- 1
5475[3]	F & D	RC-8	RC-M4c must = RC-8	- - -
5490	F & D	RC-M5c	Sum of (RC-M5a1 through RC-M5b3) must = RC-M5c	+- 3
5505[3]	F & D	RC-16	RC-M5c must = RC-16	- - -
5540	F & D	RC-M6	RC-M6 must be actual and must = "1" (yes) or "0" (no)	- - -
5600	F & D	RC-N1aC	(RC-N1aA + RC-N1aB + RC-N1aC) must <= RC-CI1aB	- - -
5620	F & D	RC-N1bC	(RC-N1bA + RC-N1bB + RC-N1bC) must <= RC-CI1bB	- - -
5640	F & D	RC-N1c1C	(RC-N1c1A + RC-N1c1B + RC-N1c1C) must <= RC-CI1c1B	- - -
5660	F & D	RC-N1c2aC	(RC-N1c2aA + RC-N1c2aB + RC-N1c2aC) must <= RC-CI1c2aB	- - -
5662	F & D	RC-N1c2aC	Sum of (RC-CIM2a1 through RC-CIM2a6 + RC-N1c2aC) must = RC-CI1c2aB **DELETE**	+- 4
5664	F	RC-N1c2aC	Sum of (RC-CIM2b1 through RC-CIM2b6 + RC-N1aC through RC-N8bC minus RC-N1c2aC) must = Sum of (RC-CI1A through RC-CI10bA minus RC-CI1c2aB **DELETE**	+- 10

EDIT #	FORM	Target ITEM	EDIT TEST	Tolerance
5664	D	RC-N1c2aC	Sum of (RC-CIM2b1 through RC-CIM2b6 + RC-N1aC through RC-N8C minus RC-N1c2aC) must = Sum of (RC-CI1aB through RC-CI10B minus RC-CI1c2aB **DELETE**	+- 10
5670	F & D	RC-N1c2bC	(RC-N1c2bA + RC-N1c2bB + RC-N1c2bC) must <= RC-CI1c2bB	- - -
5680	F & D	RC-N1dC	(RC-N1dA + RC-N1dB + RC-N1dC) must <= RC-CI1dB	- - -
5700	F & D	RC-N1eC	(RC-N1eA + RC-N1eB + RC-N1eC) must <= RC-CI1eB	- - -
5720	F	RC-N1fC	(RC-N1fA + RC-N1fB + RC-N1fC) must <= ((RC-CI1A) minus the sum of RC-CI1aB through RC-CI1eB)	- - -
5740	D	RC-N2C	(RC-N2A + RC-N2B + RC-N2C) must <= RC-CI2B	- - -
5760	F	RC-N2aC	(RC-N2aA + RC-N2aB + RC-N2aC) must <= (RC-CI2a2A + RC-CI2bA + RC-CI2c1A)	- - -
5780	F	RC-N2bC	(RC-N2bA + RC-N2bB + RC-N2bC) must <= (RC-CI2a1A + RC-CI2c2A)	- - -
5800	F	RC-N3C	(RC-N3A + RC-N3B + RC-N3C) must <= RC-CI3A	- - -
5820	D	RC-N4C	(RC-N4A + RC-N4B + RC-N4C) must <= RC-CI4B	- - -
5840	F	RC-N4aC	(RC-N4aA + RC-N4aB + RC-N4aC) must <= RC-CI4aA	- - -
5860	F	RC-N4bC	(RC-N4bA + RC-N4bB + RC-N4bC) must <= RC-CI4bA	- - -
5880	F	RC-N5aC	(RC-N5aA + RC-N5aB + RC-N5aC) must <= RC-CI6aA	- - -
5880	D	RC-N5aC	(RC-N5aA + RC-N5aB + RC-N5aC) must <= RC-CI6aB	- - -
5900	F	RC-N5bC	(RC-N5bA + RC-N5bB + RC-N5bC) must <= (RC-CI6bA + RC-CI6cA)	- - -

EDIT #	FORM	Target ITEM	EDIT TEST	Tolerance
5900	D	RC-N5bC	(RC-N5bA + RC-N5bB + RC-N5bC) must <= (RC-CI6bB + RC-CI6cB)	- - -
5920	F	RC-N6C	(RC-N6A + RC-N6B + RC-N6C) must <= RC-CI7A	- - -
5920	D	RC-N6C	(RC-N6A + RC-N6B + RC-N6C) must <= RC-CI7B	- - -
5940	F	RC-N7C	(RC-N7A + RC-N7B + RC-N7C) must <= (RC-CI8A + RC-CI9A)	- - -
5940	D	RC-N7C	(RC-N7A + RC-N7B + RC-N7C) must <= (RC-CI3B + RC-CI8B + RC-CI9B)	- - -
5960	D	RC-N8C	(RC-N8A + RC-N8B + RC-N8C) must <= RC-CI10B	- - -
5980	F	RC-N8aC	(RC-N8aA + RC-N8aB + RC-N8aC) must <= RC-CI10aA	- - -
6000	F	RC-N8bC	(RC-N8bA + RC-N8bB + RC-N8bC) must <= RC-CI10bA	- - -
6020	F	RC-N10A	RC-N10A must <= Sum of (RC-N1aA through RC-N8bA)	- - -
6020	D	RC-N10A	RC-N10A must <= Sum of (RC-N1aA through RC-N8A)	- - -
6040	F	RC-N10B	RC-N10B must <= Sum of (RC-N1aB through RC-N8bB)	- - -
6040	D	RC-N10B	RC-N10B must <= Sum of (RC-N1aB through RC-N8B)	- - -
6060	F	RC-N10C	RC-N10C must <= Sum of (RC-N1aC through RC-N8bC)	- - -
6060	D	RC-N10C	RC-N10C must <= Sum of (RC-N1aC through RC-N8C)	- - -
6080	F & D	RC-N10aA	RC-N10aA must <= RC-N10A	- - -
6100	F & D	RC-N10aB	RC-N10aB must <= RC-N10B	- - -
6120	F & D	RC-N10aC	RC-N10aC must <= RC-N10C	- - -

EDIT #	FORM	Target ITEM	EDIT TEST	Tolerance
6140	F	RC-NM1A	RC-NM1A must <= Sum of (RC-N1aA through RC-N8bA)	- - -
6140	D	RC-NM1A	RC-NM1A must <= Sum of (RC-N1aA through RC-N8A)	- - -
6160	F	RC-NM1B	RC-NM1B must <= Sum of (RC-N1aB through RC-N8bB)	- - -
6160	D	RC-NM1B	RC-NM1B must <= Sum of (RC-N1aB through RC-N8B)	- - -
6180	F	RC-NM1C	RC-NM1C must <= Sum of (RC-N1aC through RC-N8bC)	- - -
6180	D	RC-NM1C	RC-NM1C must <= Sum of (RC-N1aC through RC-N8C)	- - -
6200	F	RC-NM2A	RC-NM2A must <= (RC-N4aA + RC-N4bA + RC-N7A)	- - -
6200	D	RC-NM2A	RC-NM2A must <= (RC-N4A + RC-N7A)	- - -
6220	F	RC-NM2B	RC-NM2B must <= (RC-N4aB + RC-N4bB + RC-N7B)	- - -
6220	D	RC-NM2B	RC-NM2B must <= (RC-N4B + RC-N7B)	- - -
6240	F	RC-NM2C	RC-NM2C must <= (RC-N4aC+ RC-N4bC + RC-N7C)	- - -
6240	D	RC-NM2C	RC-NM2C must <= (RC-N4C + RC-N7C)	- - -
6260	F & D	RC-NM2C	(RC-NM2A + RC-NM2B + RC-NM2C) must <= RC-CIM3	- - -
6280	F	RC-NM3A	RC-NM3A must <= Sum of (RC-N1aA through RC-N1fA)	- - -
6280	D	RC-NM3aA	RC-NM3aA must <= Sum of (RC-N1aA through RC-N1eA)	- - -
6300	F	RC-NM3B	RC-NM3B must <= Sum of (RC-N1aB through RC-N1fB)	- - -
6300	D	RC-NM3aB	RC-NM3aB must <= Sum of (RC-N1aB through RC-N1eB)	- - -

EDIT #	FORM	Target ITEM	EDIT TEST	Tolerance
6320	F	RC-NM3C	RC-NM3C must <= Sum of (RC-N1aC through RC-N1fC)	- - -
6320	D	RC-NM3aC	RC-NM3aC must <= Sum of (RC-N1aC through RC-N1eC)	- - -
6325	F	RC-NM3C	Sum of (RC-NM3A, RC-NM3B, and RC-NM3C) must <= RC-CIM5 (rcfd1248 + rcfd1249 + rcfd1250) le (rcfdb837)	- - -
6325	D	RC-NM3aC	If RC-12 (previous June) is >= $300 million (W-code = 8 or 9), then sum of (RC-NM3aA, RC-NM3aB, and RC-NM3aC) must <= RC-CIM5 If (wcode eq 8 or wcode eq 9) then (rcon1248 + rcon1249 + rcon1250) le Rconb837	- - -
6330	D	RC-NM3bA	RC-NM3bA must <= RC-N2A	- - -
6340	D	RC-NM3bB	RC-NM3bB must <= RC-N2B	- - -
6350	D	RC-NM3bC	RC-NM3bC must <= RC-N2C	- - -
6360	D	RC-NM3cA	RC-NM3cA must <= RC-N4A	- - -
6370	D	RC-NM3cB	RC-NM3cB must <= RC-N4B	- - -
6380	D	RC-NM3cC	RC-NM3cC must <= RC-N4C	- - -
6390	D	RC-NM3dA	RC-NM3dA must <= RC-N8A	- - -
6400	D	RC-NM3dB	RC-NM3dB must <= RC-N8B	- - -
6410	D	RC-NM3dC	RC-NM3dC must <= RC-N8C	- - -
6420	D	RC-NM4A	RC-NM4A must <= RC-N7A	- - -
6430	D	RC-NM4B	RC-NM4B must <= RC-N7B	- - -

EDIT #	FORM	Target ITEM	EDIT TEST	Tolerance
6440	D	RC-NM4C	RC-NM4C must <= RC-N7C	- - -
6450	F	RC-NM5A	RC-NM5A must <= Sum of (RC-N1aA through RC-N8bA)	- - -
6450	D	RC-NM5A	RC-NM5A must <= Sum of (RC-N1aA through RC-N8A)	- - -
6460	F	RC-NM5B	RC-NM5B must <= Sum of (RC-N1aB through RC-N8bB)	- - -
6460	D	RC-NM5B	RC-NM5B must <= Sum of (RC-N1aB through RC-N8B)	- - -
6470	F	RC-NM5C	RC-NM5C must <= Sum of (RC-N1aC through RC-N8bC)	- - -
6470	D	RC-NM5C	RC-NM5C must <= Sum of (RC-N1aC through RC-N8C)	- - -
6480	F & D	RC-NM5C	(RC-NM5A + RC-NM5B + RC-NM5C) must <= RC-4a	- - -
6710	F	RC-O5c	RC-O5c must <= RC-G1b	- - -
6740	F & D	RC-O8a2	RC-O8a2 must <= RC-O8a1	- - -
6750	F & D	RC-O10	RC-O10 must <= RC-13a	- - -
6760[5]	F & D	RC-OM1a2	(JUNE) If RC-OM1a1 > 0, then RC-OM1a2 must > 0 and (RC-OM1a1 / RC-OM1a2) must <= $100 thousand	- - -
6770	F	RC-OM1b1	(RC-OM1a1 + RC-OM1b1) must = (RC-13a + RC-O5a + RC-O5b	+- 2
6770	D	RC-OM1b1	(RC-OM1a1 + RC-OM1b1) must = RC-13a	+- 1
6780[5]	F & D	RC-OM1b2	If RC-OM1b1 > 0, then (RC-OM1b1 / RC-OM1b2) must > $100 thousand	- - -
6790	F & D	RC-OM2	If RC-OM1b1 > 0, then RC-OM2 must > 0	- - -

EDIT #	FORM	Target ITEM	EDIT TEST	Tolerance
7005[3]	F & D	RC-28	RC-R1 must = RC-28	- - -
7010	F & D	RC-R8	(RC-R1 + RC-R6) minus (RC-R2 + RC-R3 + RC-R4 + RC-R5 + RC-R7) must = RC-R8	+- 4
7020	F & D	RC-R11	(RC-R8 + RC-R10) minus (RC-R9a + RC-R9b) must = RC-R11	+- 2
7040	F & D	RC-R17	Sum of (RC-R12 through RC-R16) must = RC-R17	+- 3
7060	F & D	RC-R18	If RC-R17 <= RC-R11 then RC-R18 must = RC-R17	+- 1
7065	F & D	RC-R18	If RC-R11 > 0 and RC-R17 > RC-R11, then RC-R18 must = RC-R11	+- 1
7070	F & D	RC-R18	If RC-R11 <= 0, then RC-R18 must = 0	- - -
7080	F & D	RC-R21	(RC-R11 + RC-R18 + RC-R19) minus RC-R20 must = RC-R21	+- 2
7090[3]	F & D	RC-K9	RC-R22 must = RC-K9	- - -
7100[3]	F & D	RC-R7	RC-R23 must = RC-R7	- - -
7110[3]	F & D	RC-R9a	RC-R24 must = RC-R9a	- - -
7120[3]	F & D	RC-R9b	RC-R25 must = RC-R9b	- - -
7130	F & D	RC-R27	RC-R22 minus (Sum of RC-R23 through RC-R26) must = RC-R27	+- 3
7140	F & D	RC-R34A	RC-R34A must = (RC-1a + RC-1b)	+- 1
7150	F & D	RC-R34F	(RC-R34C + RC-R34D + RC-R34F) must = RC-R34A	+- 2
7160[3]	F & D	RC-2a	RC-R35A must = RC-2a	- - -

EDIT #	FORM	Target ITEM	EDIT TEST	Tolerance
7170	F & D	RC-R35F	(RC-R35B + RC-R35C + RC-R35D + RC-R35E + RC-R35F) must = RC-R35A	+- 3
7180[3]	F & D	RC-2b	RC-R36A must = RC-2b	- - -
7190	F & D	RC-R36F	(RC-R36B + RC-R36C + RC-R36D + RC-R36E + RC-R36F) must = RC-R36A	+- 3
7200	F & D	RC-R37A	RC-R37A must = (RC-3a + RC-3b)	+- 1
7210	F & D	RC-R37F	(RC-R37C + RC-R37D + RC-R37F) must = RC-R37A	+- 2
7220[3]	F & D	RC-4a	RC-R38A must = RC-4a	- - -
7230	F & D	RC-R38f	(RC-R38B + RC-R38C + RC-R38D + RC-R38E + RC-R38F) must = RC-R38A	+- 3
7240[3]	F & D	RC-4b	RC-R39A must = RC-4b	- - -
7250	F & D	RC-R39F	(RC-R39B + RC-R39C + RC-R39D + RC-R39E + RC-R39F) must = RC-R39A	+-3
7260[3]	F & D	RC-4c	RC-R40A must = RC-4c	- - -
7270[3]	F & D	RC-4c	RC-R40B must = RC-4c	- - -
7280[3]	F & D	RC-5	RC-R41A must = RC-5	- - -
7290	F & D	RC-R41F	(RC-R41B + RC-R41C + RC-R41D + RC-R41E + RC-R41F) must = RC-R41A	+- 3
7300	F & D	RC-R42A	RC-R42A must = (RC-6 + RC-7 + RC-8 + RC-9 + RC-10a + RC-10b + RC-11)	+- 5

EDIT #	FORM	Target ITEM	EDIT TEST	Tolerance
7310	F & D	RC-R42F	(RC-R42B + RC-R42C + RC-R42D + RC-R42E + RC-R42F) must = RC-R42A	+- 3
7320[3]	F & D	RC-12	RC-R43A must = RC-12	- - -
7330	F & D	RC-R43A	(RC-R34A + RC-R35A + RC-R36A + RC-R37A + RC-R38A + RC-R39A + RC-R41A + RC-R42A) minus RC-R40A must = RC-R43A	+- 6
7340	F & D	RC-R43B	(RC-R35B + RC-R36B + RC-R38B + RC-R39B + RC-R41B + RC-R42B) minus RC-R40B must = RC-R43B	+- 5
7350	F & D	RC-R43C	(RC-R34C + RC-R35C + RC-R36C + RC-R37C + RC-R38C + RC-R39C + RC-R41C + RC-R42C) must = RC-R43C	+- 5
7360	F & D	RC-R43D	(RC-R34D + RC-R35D + RC-R36D + RC-R37D + RC-R38D + RC-R39D + RC-R41D + RC-R42D) must = RC-R43D	+- 5
7370	F & D	RC-R43E	(RC-R35E + RC-R36E + RC-R38E + RC-R39E + RC-R41E + RC-R42E) must = RC-R43E	+- 5
7380	F & D	RC-R43F	(RC-R34F + RC-R35F + RC-R36F + RC-R37F + RC-R38F + RC-R39F + RC-R41F + RC-R42F) must = RC-R43F	+- 5
7390	F & D	RC-R43F	(RC-R43B + RC-R43C + RC-R43D + RC-R43E + RC-R43F) must = RC-R43A	+- 3
7420	F & D	RC-R44F	(RC-R44C + RC-R44D + RC-R44E + RC-R44F) must = RC-R44B	+- 2
7430[3]	F & D	RC-L3	RC-R45A must = RC-L3	- - -
7440	F & D	RC-R45B	RC-R45B must = (RC-R45A x 50%)	+- 1
7450	F & D	RC-R45F	(RC-R45C + RC-R45D + RC-R45E + RC-R45F) must = RC-R45B	+- 2
7460[3]	F & D	RC-L4	RC-R46A must = RC-L4	- - -

EDIT #	FORM	Target ITEM	EDIT TEST	Tolerance
7470	F & D	RC-R46B	RC-R46B must = (RC-R46A x 20%)	+- 1
7480	F & D	RC-R46F	(RC-R46C + RC-R46D + RC-R46E + RC-R46F) must = RC-R46B	+- 2
7490	F & D	RC-R47B	RC-R47B must = RC-R47A	+- 1
7500	F & D	RC-R47F	(RC-R47C + RC-R47D + RC-R47F) must = RC-R47B	+- 2
7510[3]	F & D	RC-L6	RC-R48A must = RC-L6	- - -
7520	F & D	RC-R48B	RC-R48B must = RC-R48A	+- 1
7530	F & D	RC-R48F	(RC-R48C + RC-R48D + RC-R48E + RC-R48F) must = RC-R48B	+- 2
7540	F & D	RC-R49B	RC-R49B must = RC-R49A	+- 1
7550	F & D	RC-R49F	(RC-R49C + RC-R49D + RC-R49E + RC-R49F) must = RC-R49B	+- 2
7560	F & D	RC-R50F	RC-R50F must = RC-R50B	+- 1
7570	F & D	RC-R51B	RC-R51B must = RC-R51A	+- 1
7580	F & D	RC-R51F	(RC-R51C + RC-R51D + RC-R51E + RC-R51F) must = RC-R51B	+- 2
7590	F & D	RC-R52B	RC-R52B must = RC-R52A	+- 1
7600	F & D	RC-R52F	(RC-R52C + RC-R52D + RC-R52E + RC-R52F) must = RC-R52B	+- 2
7610	F & D	RC-R53B	RC-R53B must = (RC-R53A x 50%)	+- 1
7620	F & D	RC-R53F	(RC-R53C + RC-R53D + RC-R53E + RC-R53F) must = RC-R53B	+- 2
7630	F & D	RC-R54E	(RC-R54C + RC-R54D + RC-R54E) must = RC-R54B	+- 2

EDIT #	FORM	Target ITEM	EDIT TEST	Tolerance
7640	F & D	RC-R55C	(RC-R43C + RC-R44C + RC-R45C + RC-R46C + RC-R47C + RC-R48C + RC-R49C + RC-R51C + RC-R52C + RC-R53C + RC-R54C) must = RC-R55C	+- 6
7650	F & D	RC-R55D	(RC-R43D + RC-R44D + RC-R45D + RC-R46D + RC-R47D + RC-R48D + RC-R49D + RC-R51D + RC-R52D + RC-R53D + RC-R54D) must = RC-R55D	+- 6
7660	F & D	RC-R55E	(RC-R43E + RC-R44E + RC-R45E + RC-R46E + RC-R48E + RC-R49E + RC-R51E + RC-R52E + RC-R53E + RC-R54E) must = RC-R55E	+- 6
7670	F & D	RC-R55F	(RC-R43F + RC-R44F + RC-R45F + RC-R46F + RC-R47F + RC-R48F + RC-R49F + RC-R50F + RC-R51F + RC-R52F + RC-R53F) must = RC-R55F	+- 6
7680	F & D	RC-R57C	RC-R57C must = 0	- - -
7690	F & D	RC-R57D	RC-R57D must = (RC-R55D x 20%)	+- 1
7700	F & D	RC-R57E	RC-R57E must = (RC-R55E x 50%)	+- 1
7710	F & D	RC-R57F	RC-R57F must = RC-R55F	+- 1
7720	F & D	RC-R59F	(RC-R57C + RC-R57D + RC-R57E + RC-R57F + RC-R58F) must = RC-R59F	+- 3
7725	F & D	RC-R60F	(RC-R14 + RC-R60F) must = (RC-4c + RC-G3)	+- 2
7730	F & D	RC-R62F	RC-R62F must = RC-R59F minus (RC-R60F + RC-R61F)	+- 3

EDIT #	FORM	Target ITEM	EDIT TEST	Tolerance
8000	F & D	RC-S2bA	(RC-S2aA + RC-S2bA) must <= RC-S1A	- - -
8005	F & D	RC-S2bB	(RC-S2aB + RC-S2bB) must <= RC-S1B	- - -
8010	F & D	RC-S2bC	(RC-S2aC + RC-S2bC) must <= RC-S1C	- - -
8015	F & D	RC-S2bD	(RC-S2aD + RC-S2bD) must <= RC-S1D	- - -
8020	F & D	RC-S2bE	(RC-S2aE + RC-S2bE) must <= RC-S1E	- - -
8025	F & D	RC-S2bF	(RC-S2aF + RC-S2bF) must <= RC-S1F	- - -
8030	F & D	RC-S2bG	(RC-S2aG + RC-S2bG) must <= RC-S1G	- - -
8050	F & D	RC-S3A	RC-S3A must <= RC-S1A	- - -
8055	F & D	RC-S3B	RC-S3B must <= RC-S1B	- - -
8060	F & D	RC-S3C	RC-S3C must <= RC-S1C	- - -
8065	F & D	RC-S3D	RC-S3D must <= RC-S1D	- - -
8070	F & D	RC-S3E	RC-S3E must <= RC-S1E	- - -
8075	F & D	RC-S3F	RC-S3F must <= RC-S1F	- - -
8080	F & D	RC-S3G	RC-S3G must <= RC-S1G	- - -
8100	F & D	RC-S4bA	(RC-S4aA + RC-S4bA) must <= RC-S1A	- - -
8105	F & D	RC-S4bB	(RC-S4aB + RC-S4bB) must <= RC-S1B	- - -
8110	F & D	RC-S4bC	(RC-S4aC + RC-S4bC) must <= RC-S1C	- - -

EDIT #	FORM	Target ITEM	EDIT TEST	Tolerance
8115	F & D	RC-S4bD	(RC-S4aD + RC-S4bD) must <= RC-S1D	- - -
8120	F & D	RC-S4bE	(RC-S4aE + RC-S4bE) must <= RC-S1E	- - -
8125	F & D	RC-S4bF	(RC-S4aF + RC-S4bF) must <= RC-S1F	- - -
8130	F & D	RC-S4bG	(RC-S4aG + RC-S4bG) must <= RC-S1G	- - -
8150	F & D	RC-S7bB	(RC-S7aB + RC-S7bB) must <= RC-S6aB	- - -
8155	F & D	RC-S7bC	(RC-S7aC + RC-S7bC) must <= RC-S6aC	- - -
8160	F & D	RC-S7bF	(RC-S7aF + RC-S7bF) must <= RC-S6aF	- - -
8180	F & D	RC-S12A	RC-S12A must <= RC-S11A	- - -
8185	F & D	RC-S12B	RC-S12B must <= RC-S11B	- - -
8190	F & D	RC-S12C	RC-S12C must <= RC-S11C	- - -
8195	F & D	RC-S12D	RC-S12D must <= RC-S11D	- - -
8200	F & D	RC-S12E	RC-S12E must <= RC-S11E	- - -
8205	F & D	RC-S12F	RC-S12F must <= RC-S11F	- - -
8210	F & D	RC-S12G	RC-S12G must <= RC-S11G	- - -
8220[3]	F & D	RC-R49A	RC-SM1b must = RC-R49A	- - -
8225	F & D	RC-SM1b	RC-SM1b must <= RC-SM1a	- - -
8300	F & D	RC-T9A	Sum of (RC-T4A through RC-T8A) must = RC-T9A	+- 4

EDIT #	FORM	Target ITEM	EDIT TEST	Tolerance
8305	F & D	RC-T9B	Sum of (RC-T4B through RC-T8B) must = RC-T9B	+- 3
8310	F & D	RC-T9C	Sum of (RC-T4C through RC-T8C) must = RC-T9C	+- 2
8315	F & D	RC-T9D	Sum of (RC-T4D through RC-T8D) must = RC-T9D	+- 2
8330	F	RC-T11A	RC-T11A must <= RC-T9A	- - -
8335	F	RC-T11B	RC-T11B must <= (RC-T9B + RC-T10B)	- - -
8340	F	RC-T11C	RC-T11C must <= RC-T9C	- - -
8345	F	RC-T11D	RC-T11D must <= (RC-T9D + RC-T10D)	- - -
8360[3]	F & D	RI-5a	RC-T19 must = RI-5a	- - -
8365	F & D	RC-T19	If Sum of (RC-T12 through RC-T18) not = 0, then Sum of (RC-T12 through RC-T18) must = RC-T19	+- 5
8370	F	RC-T19a	RC-T19a must <= RC-T19	- - -
8375	F & D	RC-T23	If RC-T23 not = 0, then (RC-T19 + RC-T22) minus (RC-T20 + RC-T21) must = RC-T23	+- 2
8400[3]	F & D	RC-T4A	RC-TM1L must = RC-T4A	- - -
8405	F & D	RC-TM1L	If the sum of (RC-TM1a through RC-TM1k) is not equal to zero, then sum of (RC-TM1a through RC-TM1k) must = RC-TM1L If (rcfdb913 + rcfdb914 + rcfdb915 + rcfdb916 + rcfdb917 + rcfdb918 + rcfdb919 + rcfdb920 + rcfdb921 + rcfdb922 + rcfdb923) ne 0 then (rcfdb913 + rcfdb914 + rcfdb915 + rcfdb916 + rcfdb917 + rcfdb918 + rcfdb919 + rcfdb920 + rcfdb921 + rcfdb922 + rcfdb923) eq rcfdb868 (Note: same for 041 using "rcon")	+- 6
8420	F & D	RC-TM3hA	Sum of (RC-TM3aA through RC-TM3gA) must = RC-TM3hA	+- 2
8425	F & D	RC-TM3hB	Sum of (RC-TM3aB through RC-TM3gB) must = RC-TM3hB	+- 4

EDIT #	FORM	Target ITEM	EDIT TEST	Tolerance
8440	F & D	RC-TM4eA	Sum of (RC-TM4aA through RC-TM4dA) must = RC-TM4eA	+- 2
8445	F & D	RC-TM4eB	Sum of (RC-TM4aB through RC-TM4dB) must = RC-TM4eB	+- 2
8450	F & D	RC-TM4eC	Sum of (RC-TM4aC through RC-TM4dC) must = RC-TM4eC	+- 2
8455	F & D	RC-TM4eC	(RC-TM4eA + RC-TM4eB) minus RC-TM4eC must = RC-T21	+- 2
8500	F & D	RC-SRb	If RC-SRa > 0, then RC-SRb must > 0	- - -
8510	F & D	RC-SRb	If RC-SRb > 0, then RC-SRa must > 0	- - -
8520	F & D	RC-SRc2	RC-SRc1 must <= RC-SRc2	- - -

[3] The item is computer-generated. <u>First occurrence should be mapped over into second occurrence of the same MDRM number.</u>

[4] This edit uses both the Report of Condition and Income.

[1] Reporting requirements based on asset thresholds are generally based on "previous June" assets. The following criteria should be applied:

Form	FRB W-Code	Previous June Assets Threshold
FFIEC 031	1	Less than $1 Billion <u>with</u> IBF including foreign offices
	2	Less than $1 Billion <u>without</u> IBF including foreign offices
	3	Greater than or equal to $1 Billion <u>with</u> IBF including foreign offices
	4	Greater than or equal to $1 Billion <u>without</u> IBF including foreign offices
FFIEC 041	5	Less than $25 Million
	6	Greater than or equal to $25 Million, but less than $100 Million
	7	Greater than or equal to $100 Million, but less than $300 Million
	8	Greater than or equal to $300 Million, but less than $1 Billion
	9	Greater than or equal to $1 Billion

[5] This is a 'size' error edit (zero divide). The edit should fail when the denominator is zero.